

<b>Notice of References Cited</b>	Application/Control No. 10/043,654	Applicant(s)/Patent Under Reexamination BUNKER V. ET AL	
	Examiner Tongoc Tran	Art Unit 2134	Page 1 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,088,804	07-2000	Hill et al.	726/25
*	B	US-6,226,372	05-2001	Beebe et al.	379/189
*	C	US-6,253,337	06-2001	Maloney et al.	714/38
*	D	US-6,298,445	10-2001	Shostack et al.	726/25
*	E	US-6,766,458	07-2004	Harris et al.	726/6
*	F	US-6,484,315	11-2002	Ziese, Kevin J.	717/173
*	G	US-6,574,737	06-2003	Kingsford et al.	726/25
*	H	US-6,766,458	07-2004	Harris et al.	726/6
*	I	US-6,782,527	08-2004	Kouznetsov et al.	717/103
*	J	US-2001/0034847	10-2001	Gaul, Jr., Stephen E.	713/201
*	K	US-2002/0026591	02-2002	Hartley et al.	713/201
*	L	US-6,292,822	09-2001	Hardwick, Jonathan C.	718/105
*	M	US-6,269,330	07-2001	Cidon et al.	714/43

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Li et al., Proceedings of 2000 Internal Parallel and Distributed Processing Symposium (IPDPS'00), May 2000, PP. 431-438. <a href="http://www.cs.wm.edu/hpcs/WWW/HTML/publications/abs00-2.html">http://www.cs.wm.edu/hpcs/WWW/HTML/publications/abs00-2.html</a>
	V	Zhang et al., "Modeling and characterizing Parallel Computing Performance on Heterogeneous Networks of Workstations", Proceedings of 7th IEEE Symposium on Parallel and Distributed Processing (SPDP'95), October 1995.
	W	Mache, "Parallel I/O and Communication -Sensitive Scheduling on High Performance Parallel Computers", Ph.D. thesis., 1/6/1999. <a href="http://www.lclark.edu/~jmache/diss.html">http://www.lclark.edu/~jmache/diss.html</a> . ✓
	X	<a href="http://web.archive.org/web/19990117014320/http://java.sun.com/products/jdbc/overview.html">http://web.archive.org/web/19990117014320/http://java.sun.com/products/jdbc/overview.html</a> . , 1999 ✓

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are not publication dates. Classifications may be US or foreign.

# **Notice of References Cited**

Application/Control No.

10/043,654

Applicant(s)/Patent Under  
Reexamination  
BUNKER V. ET AL

Examiner

Tongoc Tran

Art Unit

2134

Page 2 of 2

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Falsafi et al., "Cost/Performance of a Parallel Computer Simulator", Computer Science Department, University of Wisconsin Madison, pages 173-182. , 1994 , Workshop on parallel computer simulator ✓
	V	Polk, "Automated Tools for Testing Computer Systems Vulnerability", <a href="http://www.nsi.org/Library/Compsec/CSECTOOL.TXT">http://www.nsi.org/Library/Compsec/CSECTOOL.TXT</a> , 1993
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.